

**Smart phones** 

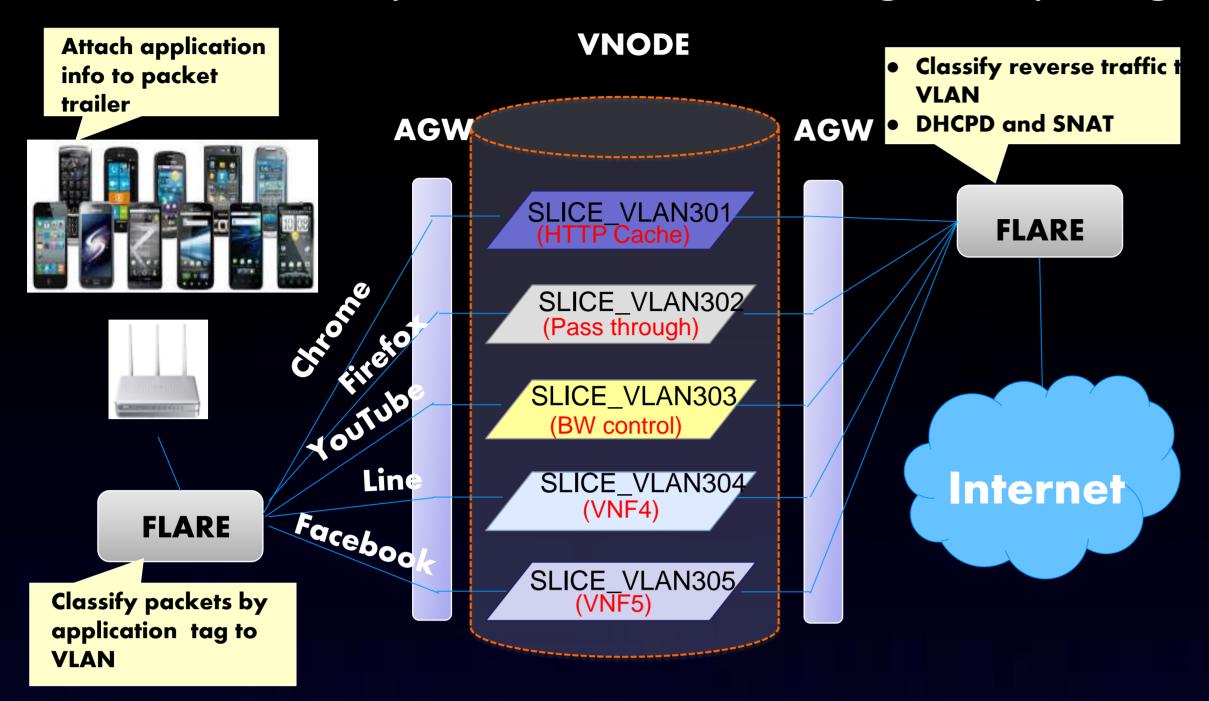
# HARE

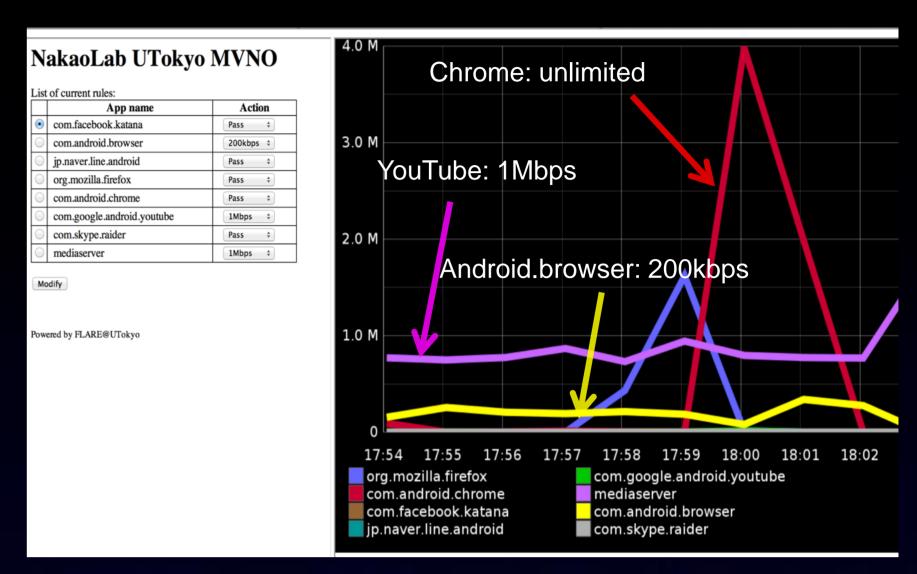
Open Deeply Programmable Network Node Architecture

## **Application Specific QoS Control**

Smart phones attach meta info. on App (devices) to packets.

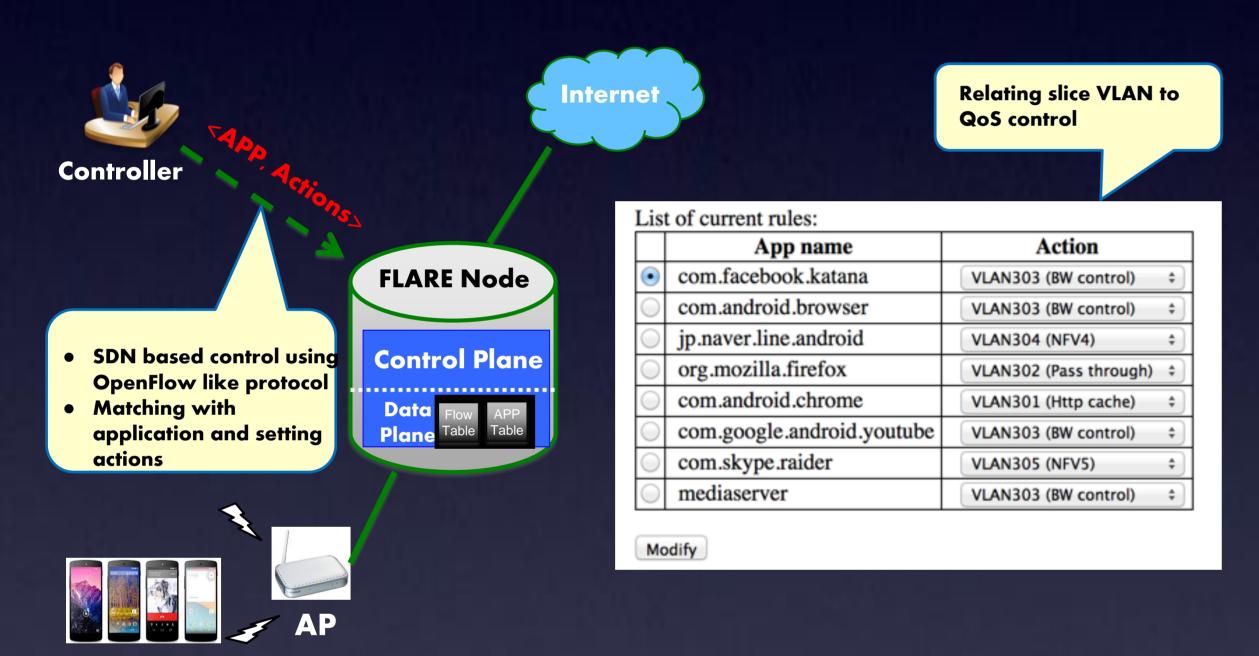
FLARE classifies packets to different edge computing slices according to application specific QoS.

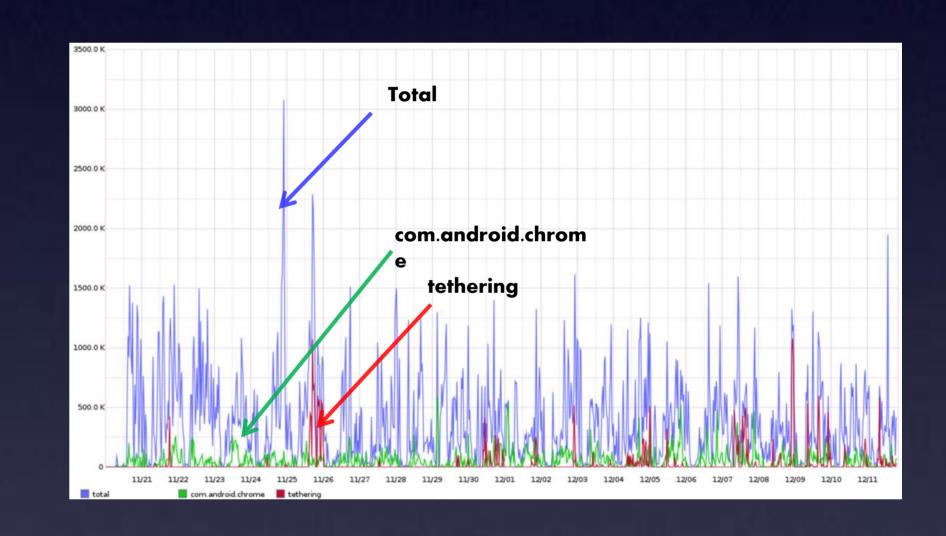




### **Application Specific Edge Computing**

Application Specific QoS via Custom Southbound Interface

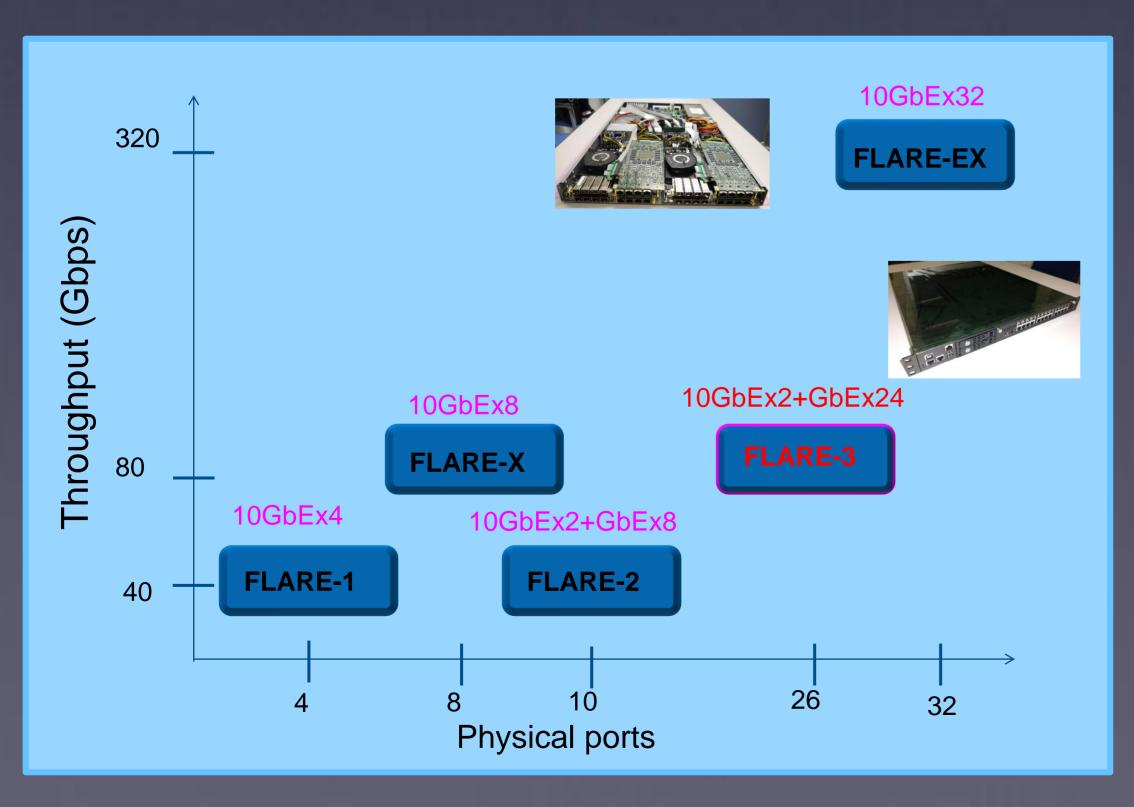


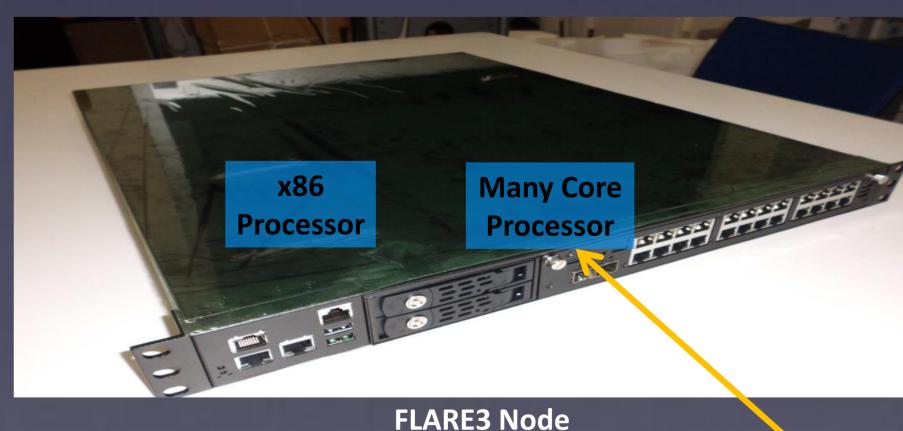


Web-GUI based Application Control

### **Application Specific Traffic Monitor**

## FLARE Node Development





#### x86 CPU (LXC on top)

- 72 core EZ-Chip NPU (LXC on Top)
- GbE: 24ports and 10GbE SFP+: 2 ports (or 10GbE SFP+ : 8 ports)
- Up to 128GB memory for NPU
- Redundant Power supply
- Swappable SSD x2

MicA

DDR3 Controller

DDR3 Controller

Wetwork I/O

REAL SECOND SECOND

FLARE 3

lineup

THE UNIVERSITY OF TOKYO

FLARE node lineup